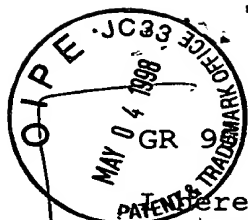


0300



GR 9 P 1411 P

#5/A
6-6-98
T.T.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

By:

Date:

April 28, 1998

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gunther Plasa
Serial No. : 09/020,872
Filed : February 9, 1998
Title : Method for Producing a Memory Cell

P R E L I M I N A R Y A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Preliminary to examination, kindly amend the above-identified application as follows:

In the Specification:

Page 12, line 3, after "capacitance" insert, "and as a control gate of the EEPROM".

Remarks:

Reconsideration of the application is requested.

The application has been revised in order to more clearly describe the invention. Support for the insertion on page 12 of the specification can be found on page 5, lines 10-17. The

17

A